


Form PTO-1449 (modified) List of Patents and Publications For Applicant's Information Disclosure Statement (Use several sheets if necessary)		ATTY. DOCKET NO: 5150-23007		SERIAL NO: 09/788,104			
							
		APPLICANT: Kodosky et al.					
FILING DATE: February 16, 2001		GROUP: 2128					
U.S. PATENT DOCUMENTS							
EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
RF	C1	4,901,221	Feb. 13, 1990	Kodosky, et al.	715	771	
FOREIGN PATENT DOCUMENTS							
EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION. YES/NO
RF	C2	DE 692 32 869 T2	Sept. 4, 2003	Germany	—	—	YES
RF	C3	DE 42 05 524 A1	Aug. 27, 1992	Germany	—	—	YES
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
RF	C4	Levy, M., "DSP Design Tools Target FPGAs," www.reed-electronics.com/ednmag/archives/1996/062096/13df2.htm , June 20, 1996.					
RF	C5	Heimdahl, M.P.E., Keenan, D.J., "Generating Code from Hierarchical State-Based Requirements," Proceedings of IEEE International Symposium on Requirements Engineering (RE'97)," January 1997.					
RF	C6	Integrated Systems, Inc., "MATRIXx Product Family Technical Specifications," Product Manual, Copyright 1995.					
RF	C7	Pauer, E.K., "Multiprocessor System Development for High Performance Signal Processing Applications," Proceedings of the 1997 IEEE Int. Workshop on Rapid System Prototyping (RSP)," Copyright 1997.					
RF	C8	Drusinsky, D., "Extended State Diagrams and Reactive Systems," Dr. Dobb's Journal, October 1994.					
RF	C9	Ade, M., Lauwereins, R., and Peperstraete, J.A., "Hardware-Software Codesign with GRAPE," Rapid System Prototyping, Proceedings of the Sixth IEEE International Workshop on Rapid System Prototyping (RSP'95), 1995.					
RF	C10	Chen, X., Ling, X., Amano, H., "Software Environment for WASMII: a Data Driven Machine with a Virtual Hardware," Proceedings of 4 th Workshop on Field-Programmable Logic and Applications (FPL '94), pages 208-219, Springer Verlag, September 1994.					
EXAMINER: <u>RUSSELL FREED</u>			DATE CONSIDERED: <u>3.17.04</u>				
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.							